

**GOVERNMENT POLYTECHNIC YAVATMAL****Dhamangaon Road Yavatmal-445001**

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No.GPY/Estt/Ad/2018/ 8840

Date 27/01/19 ✓


**Walk in Interview**

Visiting faculty on purely temporary clock hour basis is required for Electronics & Tele.Comm.Engg.

Qualification as per AICTE norms

Date &amp; Time of Interview 04.01.2019 , 12.00 Noon am Venue :- Electronics Engg.

Department Intersted candidats shall remain present with application all original documents &amp; One set of Photocopy

  
Principal

(Government Polytechnic Yavatmal Old)

Desktop/General 2017